



10/726,675. *o/c*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent of : **Attn: Certificate of Correction Branch**
Koichi HIRANO et al. :
Patent No. 7,126,811 : Atty Docket No. 2003_1690A
Issued October 24, 2006 : **Confirmation No. 5709**

CAPACITOR AND METHOD
FOR PRODUCING THE SAME, AND
CIRCUIT BOARD WITH A BUILT-IN
CAPACITOR AND METHOD FOR
PRODUCING THE SAME

REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 CFR 1.322

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 CFR 1.322, it is respectfully requested that a Certificate of Correction issue to correct the following:

Column 38,

Line 64, "through holes" should read --through hole--.

Certificate
DEC 06 2006
of Correction

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975

DEC 06 2006

REMARKS

The above-noted error in the claims apparently arose due to a PTO mistake. In particular, it is noted that the requested correction can be found in the Amendment filed on June 7, 2005. Accordingly, a Certificate of Correction should issue at no expense to patentee. Form PTO-1050 accompanies this request.

Respectfully submitted,

Koichi HIRANO et al.

By Kenneth W. Fields
Kenneth W. Fields
Registration No. 52,430
Attorney for Patentees

KWF/jmj
Washington, D.C.
Telephone (202) 721-8200
Facsimile (202) 721-8250
December 4, 2006

To: The Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO : 7,126,811
DATED : October 24, 2006
INVENTOR(S) : Koichi HIRANO et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 38,

Line 64, "through holes" should read --through hole--.

Wenderoth, Lind & Ponack, L.L.P.
2033 K Street, N. W., Suite 800
Washington, D.C. 20006-1021

PATENT NO. 7,126,811

No. of additional copies

DEC 18 2006